

PATENT ABSTRACTS

[File 347] JAPIO Dec 1976-2007/Dec(Updated 080328)
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[File 350] Derwent WPIX 1963-2008/UD=200830
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Set	Items	Postings	Description
S1	903534	4342665	S TEST???
S2	3303	26092	S MERG???(2N)(DATA OR OUTPUT? OR OUT()PUT? ? OR BIT OR BITS OR PATTERN? ? OR SIGNAL? ?)
S3	2672	20921	S (DOUBLE OR DUAL)(2N)DATA()RATE OR DDR
S4	654	3933	S SINGLE()DATA()RATE OR SDR
S5	405	3316	S S2(3N)(GENERAT??? OR MAKE? ? OR MAKING OR MADE OR CREAT???)
S6	68061	628653	S (CLOCK OR EDGE? ?)(3N)(RISE? ? OR RISING OR LEAD??? OR FALL??? OR TRAIL???)
S7	32	804	S S1 AND S5
S8	1	92	S S7 AND (S3:S4 OR S6)
S9	31	764	S S7 NOT S8
S10	769	3853	S MERG???(3N)(OUTPUT? OR OUT()PUT???)
S11	4101018	28274510	S CIRCUIT?? OR IC OR SEMICONDUCT??? OR CHIP? ?
S12	218	4977	S S10 AND S11
S13	57	394	S S10(3N)(GENERAT??? OR MAKE? ? OR MAKING OR MADE OR CREAT???)
S14	18	627	S S13 AND S11
S15	15	569	S S14 NOT S7

FULL-TEXT PATENTS

[File 348] EUROPEAN PATENTS 1978-2007/ 200818

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[File 349] PCT FULL TEXT 1979-2008/UB=20080508IUT=20080501

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Set	Items	Postings	Description
S1	722915	9467629	S TEST???
S2	5230	47612	S MERG???(2N)(DATA OR OUTPUT? OR OUT()PUT? ? OR BIT OR BITS OR PATTERN? ? OR SIGNAL? ?)
S3	6065	32436	S (DOUBLE OR DUAL)(2N)DATA()RATE OR DDR
S4	1646	14073	S SINGLE()DATA()RATE OR SDR
S5	464	4296	S S2(3N)(GENERAT??? OR MAKE? ? OR MAKING OR MADE OR CREAT???)
S6	68180	1218470	S (CLOCK OR EDGE? ?)(3N)(RISE? ? OR RISING OR LEAD??? OR FALL??? OR TRAIL???)
S7	19	144	S S1(50N)S5
S8	26	245	S S1(100N)S5
S9	0	0	S S8(100N)(S3:S4 OR S6)
S10	1029260	28905493	S CIRCUIT?? OR IC OR SEMICONDUCTOR? ? OR CHIP? ? OR PROCESSOR? ? OR MICROPROCESSOR? ?
S11	9	159	S S8(100N)S10
S12	1118	6605	S MERG???(3N)(OUTPUT? OR OUT()PUT???)
S13	71	483	S S12(3N)(GENERAT??? OR MAKE? ? OR MAKING OR MADE OR CREAT???)
S14	13	107	S S13(100N)S10
S15	13	107	S S14 NOT S11

NPL

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Set	Items	Postings	Description
S1	7852850	16126005	S TEST???
S2	6529	15733	S MERC???(2N)(DATA OR OUTPUT? OR OUT()PUT? ? OR BIT OR BITS OR PATTERN? ? OR SIGNAL? ?)
S3	8791	14928	S (DOUBLE OR DUAL)(2N)DATA(RATE OR DDR
S4	5331	12565	S SINGLE()DATA()RATE OR SDR
S5	156	488	S S2(3N)(GENERAT??? OR MAKE? ? OR MAKING OR MADE OR CREAT???)
S6	86628	327957	S (CLOCK OR EDGE? ?)(3N)(RISE? ? OR RISING OR LEAD??? OR FALL??? OR TRAIL???)
S7	16	91	S S1 AND S5
S8	27	153	S S2 AND (S3:S4 OR S6)
S9	43	244	S S7:S8
S10	27	150	RD (unique items)
S11	21	116	S S10 NOT PY=2004:2008
S12	4749021	12869523	S CIRCUIT?? OR IC OR CHIP? ? OR SEMICONDUCTOR? ? OR PROCESSOR? ? OR MICROPROCESSOR? ?

S13	575	3276	S S2 AND S12
S14	0	0	S S13 AND INVERTOR? ?
S15	116	1306	S S13 AND S1
S16	8	69	S S13 AND (S4:S5 OR S6)
S17	6	49	RD (unique items)
S18	395	882	S MERG???(3N)(OUTPUT? OR OUT()PUT???)
S19	9	27	S S18(3N)(GENERAT??? OR MAKE? ? OR MAKING OR MADE OR CREAT???)
S20	6	18	RD (unique items)
S21	5	15	S S20 NOT (S11 OR S17)
S22	14	134	S S18 AND S12 AND (CLOCK??? OR CLOCKPULSE?)
S23	8	85	RD (unique items)
S24	7	79	S S23 NOT (S11 OR S17 OR S21)